

## Datasheet

**TRF0-1**

Transmitter  
Rise/Fall Time  
Adjust

11 = -0.25ns  
10 = +0.0ns  
01 = +0.25ns  
00 = +0.50ns

**9.7 Register 17. Configuration 2 - Structure and Bit Definition**

Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
R	R	R	R	R	R	R	R
1	1	1	1	1	1	1	1

Reserved	Reserved	APOLDIS	JABDIS	MREG	INTMDIO	Reserved	Reserved
R	R	RW	RW	RW	RW	RW	RW
0	0	0	0	0	0	0	0

**APOLDIS:** Auto Polarity Disable

1 = Auto Polarity Correction Function Disabled  
0 = Normal  
1 = Jabber Disabled    RW  
0 = Enabled

**JABDIS:** Jabber Disable Select

**MREG:** Multiple Register Access Enable  
1 = Multiple Register Access Enabled  
0 = No Multiple Register Access

**INTMDIO:** Interrupt Scheme Select

1 = Interrupt Signaled With MDIO Pulse During Idle  
0 = Interrupt Not Signaled On MDIO

**Reserved:** Reserved for Factory Use**9.8 Register 18. Status Output - Structure and Bit Definition**

INT	LNKFAIL	LOSSSYNC	CWRD	SSD	ESD	RPOL	JAB
R	R/LT	R/LT	R/LT	R/LT	R/LT	R/LT	R/LT
0	0	0	0	0	0	0	0

SPDDET	DPLXDET	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
R/LT	R/LT	R	R	R	R	R	R
1	0	0	0	0	0	0	0

**INT:** Interrupt Detect  
1 = Interrupt Bit(s) Have Changed Since Last Read Operation.  
0 = No Change

**LNKFAIL:** Link Fail Detect  
1 = Link Not Detected  
0 = Normal

**LOSSSYNC:** Descrambler Loss of Synchronization Detect  
1 = Descrambler Has Lost Synchronization  
0 = Normal

**CWRD:** Codeword Error  
1 = Invalid 4B5B Code Detected On Receive Data  
0 = Normal

**SSD:** Start Of Stream Error  
1 = No Start Of Stream Delimiter Detected on Receive Data  
0 = Normal

**ESD:** End Of Stream Error  
1 = No End Of Stream Delimiter Detected on Receive Data  
0 = Normal

<b>RPOL:</b>	Reverse Polarity Detect	1 = Reverse Polarity Detected 0 = Normal
<b>JAB:</b>	Jabber Detect	1 = Jabber Detected 0 = Normal
<b>SPDDET:</b>	100/10 Speed Detect	1 = Device in 100Mbps Mode (100BASE-TX) 0 = Device in 10Mbps Mode (10BASE-T)
<b>DPLXDET:</b>	Duplex Detect	1 = Device In Full Duplex 0 = Device In Half Duplex
<b>Reserved:</b>	Reserved	Reserved for Factory Use

## 9.9 Register 19. Mask - Structure and Bit Definition

MINT	MLNKFAIL	MLOSSYN	MCWRD	MSSD	MESD	MRPOL	MJAB
RW	RW	RW	RW	RW	RW	RW	RW
1	1	1	1	1	1	1	1

MSPDDT	MDPLDT	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
RW	RW	RW	RW	RW	RW	RW	RW
1	1	0	0	0	0	0	0

**MINT:** Interrupt Mask Interrupt Detect  
1 = Mask Interrupt For INT In Register 18  
0 = No Mask

**MLNKFAIL:** Interrupt Mask Link Fail Detect  
1 = Mask Interrupt For LNKFAIL In Register 18  
0 = No Mask

**MLOSSYN:** Interrupt Mask Descrambler Loss of Synchronization Detect  
1 = Mask Interrupt For LOSSSYNC In Register 18  
0 = No Mask

**MCWRD:** Interrupt Mask Codeword Error  
1 = Mask Interrupt For CWRD In Register 18  
0 = No Mask

**MSSD:** Interrupt Mask Start Of Stream Error  
1 = Mask Interrupt For SSD In Register 18  
0 = No Mask

**MESD:** Interrupt Mask End Of Stream Error  
1 = Mask Interrupt For ESD In Register 18  
0 = No Mask

**MRPOL:** Interrupt Mask Reverse Polarity Detect  
1 = Mask Interrupt For RPOL In Register 18  
0 = No Mask

**MJAB:** Interrupt Mask Jabber Detect  
1 = Mask Interrupt For JAB In Register 18  
0 = No Mask

**MSPDDT:** Interrupt Mask 100/10 Speed Detect  
1 = Mask Interrupt For SPDDET In Register 18  
0 = No Mask

**MDPLDT:** Interrupt Mask Duplex Detect  
1 = Mask Interrupt For DPLXDET In Register 18  
0 = No Mask

**Reserved:** Reserved  
Reserved for Factory Use